

Patent Details Electronics Engineering

Sr. No.	Department	Title	Inventors	Application Number	Date of Filing
1	Electronics Engineering	Portable and Smart Electric Utility Dashboard Outlet (P.S.E.U.D.O.) Device for precondition smart phone charging	Mr. Mukesh Prajapati Mr. Ketan Salvi Mr. Girish Giri Ms Divya Shah Dr Vishwesh A. Vyawahare	201821008364	3/7/2018
2	Electronics Engineering	Earthquake Rescue and Relief Operations Robot	Vaibhav Kadam, Saket Seshadri, Pramod Jadhav, Anay Ghatpande, Divya Shah and Vishwesh Vyawahare	201821008351	3/7/2018
3	Electronics Engineering	Laboratory setup for learning fractional order control system.	Dr. Vishwesh A. Vyawahare , Dr. M. D. Patil	201721044974	12/14/2017
4	Electronics Engineering	Efficient Fractional-order Proportional-Integral-Derivative (FO-PID) Controller Implementation with Artificial Neural Network Approximation using Digital Signal Processor	Dr. Vishwesh A. Vyawahare, Mr. Pratik Kadam , Mr. Gaurav Datkhile, Dr. M. D. Patil	201721047042	12/28/2017
5	Electronics Engineering	FPGA Based Hardware System For implementation of Complex-order Dynamical System	Ms. Divya Shah, Dr. M. D. Patil, Dr. Vishwesh Vyawahare, Mr. Rohit Chaurasiya, Ms. Khushboo Pichhode	201821000547	1/5/2017
6	Electronics Engineering	A Novel Laboratory Setup to Study Advanced Nonlinear and Robust Controller Concepts	Dr. Prasad Hirve Dr. Mukesh D. Patil Dr. Vishwesh A. Vyawahare Mr. Pratik Kadam Ms. Divya Shah Mr. Gaurav Datkhile	201821006842	10/23/2017

7	Electronics Engineering	Compact setup for experimentation on variable-order systems in laboratory	Dr. Mukesh D. Patil Dr. Sharad P. Jadhav Dr. Vishwesh A. Vyawahare	201821006844	10/23/2017
8	Electronics Engineering	A Novel Laboratory Setup to Study Advanced Nonlinear and Robust Controller Concepts	Dr. Prasad Hirve Dr. Mukesh D. Patil Dr. Vishwesh A. Vyawahare Mr. Pratik Kadam Ms. Divya Shah Mr. Gaurav Datkhile	201821006842	9/5/2017
9	Electronics Engineering	Digital Drafting apparatus for real time archotyping and supplemented digital rendering	Mr. Girish Giri Mr. Mukesh Prajapati Mr. Ketan Salvi Dr. Vishwesh A. Vyawahare Ms. Divya Shah	201821006583	9/5/2017
10	Electronics Engineering	Low-cost modular laboratory crawler bot setup to demonstrate two axis holonomic motion.	Mr. Girish Giri Mr. Mukesh Prajapati Mr. Ketan Salvi Ms. Divya Shah Dr. Vishwesh A. Vyawahare	201821006572	5/19/2017
11	Electronics Engineering	DSP Based Hardware Emulator for Complex-Order Fractional Dynamical System Using Artificial Neural Network Approximation Technique	Mr. Pratik Kadam Mr. Gaurav Datkhile Dr. Vishwesh Vyawahare Ms. Divya Shah	201821006586	5/19/2017
12	Electronics Engineering	Novel Special Function Based FPGA Hardware System for Time Domain Response Calculation of Linear Fractional-Order Systems	Mr Sangeeth Sadanand Ms Divya Shah Dr Vishwesh A. Vyawahare	201821006577	5/19/2017
13	Electronics Engineering	DSP Based Hardware Emulator for Complex-Order Fractional Dynamical System Using Artificial Neural Network Approximation Technique	Mr. Pratik Kadam Mr. Gaurav Datkhile Dr. Vishwesh Vyawahare Ms. Divya Shah	201821006586	4/12/2017
14	Electronics Engineering	IoT based Cortic Pad for sleep analysis	Mr Rohan Stanley Dr. Ramesh Vasappanavara, Ms Ananya Iyer Mr Rohit Chaurasiya Ms Divya Shah	201821006596	4/12/2017

15	Electronics Engineering	Low-cost modular laboratory crawler bot setup to demonstrate two axis holonomic motion.	Mr. Girish Giri Mr. Mukesh Prajapati Mr. Ketan Salvi Ms. Divya Shah Dr. Vishwesh A. Vyawahare	201821006572	3/27/2017
16	Electronics Engineering	Digital Drafting apparatus for real time archotyping and supplemented digital rendering	Mr. Girish Giri Mr. Mukesh Prajapati Mr. Ketan Salvi Dr. Vishwesh A. Vyawahare Ms. Divya Shah	201821006583	3/27/2017

Patent Details Computer Engineering

Sr. No.	Department	Title	Inventors	Application Number	Date of Filing
1	Computer Engineering	A technology (process) for identifying attainable, measurable and verifiable goals and objectives from vision and mission of an enterprise to enable measurement of attainment of Mission	Dr. Vasappanavara Ramesh, Mrs. Chhabria Aditi Sunil, Dr. Bhatia Manmohan Singh	201721009024	3/16/2017

Patent Details Electronics & Telecommunication Engineering

Sr. No.	Department	Title	Inventors	Application Number	Date of Filing
1	Electronics & Telecommunication Engineering	A waste segregation mechanism for dustbin to be used at the source of waste generation	Adarsh G. Nair, Jayesh R. Sharma, Sahil U. Mohite and Mukesh D. Patil	201921025816	6/28/2019
2	Electronics & Telecommunication Engineering	A Mobile Device for Mapping and Monitoring of Underground Coal Mines	Ajit G. Mutalik, Vineet Menon, Sanjivani Chakote and Mukesh D. Patil	201821049115	12/26/2018

3	Electronics & Telecommunication Engineering	Compact Setup for experimentation of Variable-order Systems in Laboratory	Mukesh D. Patil, Sharad P. Jadhav and Vishwesh A. Vyawahare	201821006844	2/23/2018
4	Electronics & Telecommunication Engineering	A Novel Laboratory Setup to Study Advanced Nonlinear and Robust Controller Concepts	Prasiddh Trivedi, Mukesh D. Patil, Vishwesh A. Vyawahare, Pratik Kadam, Divya Shah and Gaurav Datkhile	201821006842	2/23/2018
5	Electronics & Telecommunication Engineering	FPGA based Hardware System for implementation of Complex-order Dynamical System	Divya Shah, Mukesh D. Patil, Vishwesh A. Vyawahare, Rohit Chaurasiya and Khushboo Pichode	2018210000547	1/5/2018
6	Electronics & Telecommunication Engineering	Efficient Fractional-order Proportional-Integral-Derivative (FO-PID) Controller Implementation with Artificial Neural Network	Vishwesh A. Vyawahare, Pratik Kadam, Gaurav Datkhile and Mukesh D. Patil	201721047042	12/28/2017
7	Electronics & Telecommunication Engineering	Laboratory Setup for Learning Fractional-Order Control System	Vishwesh A. Vyawahare and Mukesh D. Patil	201721044974	12/14/2017

Patent Details Instrumentation Engineering

Sr. No.	Department	Title	Inventors	Application Number	Date of Filing
1	Instrumentation Engineering	Compact Set-up for experimentation on variable-order systems in laboratory	V A Vyawaharr M D Patil Sharad P Jadhav	201821006844	11/11/2018