

**Patents Filed**

<b>Sr. No.</b>	<b>Title</b>	<b>Inventors</b>	<b>Application Number</b>	<b>Date of Filing</b>	<b>Department</b>
1	Repellent Spray	Rupali N Agme	Application in process	Jul-20	Engineering Sciences
2	A waste segregation mechanism for dustbin to be used at the source of waste generation	Adarsh G. Nair, Jayesh R. Sharma, Sahil U. Mohite and Mukesh D. Patil	201921025816	Jun-19	Electronics and Telecommunication Engineering
3	A Mobile Device for Mapping and Monitoring of Underground Coal Mines	Ajit G. Mutalik, Vineet Menon, Sanjivani Chakote and Mukesh D. Patil	201821049115	Dec-18	Electronics and Telecommunication Engineering
4	Compact Set-up for experimentation on variable-order systems in laboratory	V A Vyawaharr M D Patil Sharad P Jadhav	201821006844	Nov-18	Instrumentation Engineering
5	Earthquake Rescue and Relief Operations Robot	Mr. Vaibhav N. Kadam Mr. G. H. Saket Seshadri Mr. Pramod S. Jadhav Mr. Anay D. Ghatpande Ms. Divya Shah Dr. Vishwesh A. Vyawahare	201821008351	Jul-18	Electronics Engineering
6	Portable and Smart Electric Utility Dashboard Outlet (P.S.E.U.D.O.) Device for precondition smart phone charging	Mr. Mukesh Prajapati Mr. Ketan Salvi Mr. Girish Giri Ms Divya Shah Dr Vishwesh A. Vyawahare	201821008364	Jul-18	Electronics Engineering
7	Braille printer using android smartphone	Mr. Subhrajyoti Halder Mr. Pradeep Harname Mr. Vikas Bankar Mr. Gaurav Ghatwal Prof. Sharmila J. Petkar	201821008355	Jul-18	Electronics Engineering

8	IoT Maneuvered Intelligent Cartesian Plotter System on Android Platform	Mr. Mayur Manyar Mr. Nishant Bhangale Mr. Pratik Kadam Mr. Gaurav Datkhile	201821998362	Jul-18	Electronics Engineering
9	Elevator System to Avoid False call from floor	Mr. Rohan Gavandi Mr. Shubham Mumbaikar Mr. Pratik Magadwar Ms. Poornima Talwai	201821008369	Jul-18	Electronics Engineering
10	Neural Network Based Interlinked Motorcycle Ecosystem For Smart Navigation	Mr. Rohan R. Mahajan Mr. Vijay S. Rane Mr. Heramb S. Khanvilkar Mr. Kartikey A. Kawadkar Mr. Sachin R. Iyer Mrs. Shweta S. Ashtekar	201821022808	Jun-18	Electronics Engineering
11	FPGA Based Hardware System For implementation of Complex-order Dynamical System	Ms. Divya Shah, Dr. M. D. Patil, Dr. Vishwesh Vyawahare, Mr. Rohit Chaurasiya, Ms. Khushboo Pichhode	201821000547	May-18	Electronics Engineering
12	Compact setup for experimentation on variable-order systems in laboratory	Dr. Mukesh D. Patil Dr. Sharad P. Jadhav Dr. Vishwesh A . Vyawahare	201821006844	Feb-18	Electronics Engineering
13	A Novel Laboratory Setup to Study Advanced Nonlinear and Robust Controller Concepts	Dr. Prasiddh Trivedi Dr. Mukesh D. Patil Dr. Vishwesh A. Vyawahare Mr. Pratik Kadam Ms. Divya Shah Mr. Gaurav Datkhile	201821006842	Feb-18	Electronics Engineering

14	Compact Setup for experimentation of Variable-order Systems in Laboratory	Mukesh D. Patil, Sharad P. Jadhav and Vishwesh A. Vyawahare	201821006844	Feb-18	Electronics and Telecommunication Engineering
15	A Novel Laboratory Setup to Study Advanced Nonlinear and Robust Controller Concepts	Prasiddh Trivedi, Mukesh D. Patil, Vishwesh A. Vyawahare, Pratik Kadam, Divya Shah and Gaurav Datkhile	201821006842	Feb-18	Electronics and Telecommunication Engineering
16	Novel Special Function Based FPGA Hardware System for Time Domain Response Calculation of Linear Fractional-Order Systems	Mr Sangeeth Sadanand Ms Divya Shah Dr Vishwesh A. Vyawahare	201821006577	Feb-18	Electronics Engineering
17	DSP Based Hardware Emulator for Complex-Order Fractional Dynamical System Using Artificial Neural Network Approximation Technique	Mr. Pratik Kadam Mr. Gaurav Datkhile Dr. Vishwesh Vyawahare Ms. Divya Shah	201821006586	Feb-18	Electronics Engineering
18	IoT based Cortic Pad for sleep analysis	Mr Rohan Stanley Dr. Ramesh Vasappanavara, Ms Ananya Iyer Mr Rohit Chaurasiya Ms Divya Shah	201821006596	Feb-18	Electronics Engineering
19	Low-cost modular laboratory crawler bot setup to demonstrate two axis holonomic motion.	Mr. Girish Giri Mr. Mukesh Prajapati Mr. Ketan Salvi Ms. Divya Shah Dr. Vishwesh A. Vyawahare	201821006572	Feb-18	Electronics Engineering

20	Digital Drafting apparatus for real time archotyping and supplemented digital rendering	Mr. Girish Giri Mr. Mukesh Prajapati Mr. Ketan Salvi Dr. Vishwesh A. Vyawahare Ms. Divya Shah	201821006583	Feb-18	Electronics Engineering
21	FPGA based Hardware System for implementation of Complex-order Dynamical System	Divya Shah, Mukesh D. Patil, Vishwesh A. Vyawahare, Rohit Chaurasiya and Khushboo Pichode	2018210000547	Jan-18	Electronics and Telecommunication Engineering
22	Efficient Fractional-order Proportional-Integral-Derivative (FO-PID) Controller Implementation with Artificial Neural Network Approximation using Digital Signal Processor	Dr. Vishwesh A. Vyawahare, Mr. Pratik Kadam , Mr. Gaurav Datkhile, Dr. M. D. Patil	201721047042	Dec-17	Electronics Engineering
23	Efficient Fractional-order Proportional-Integral-Derivative (FO-PID) Controller Implementation with Artificial Neural Network	Vishwesh A. Vyawahare, Pratik Kadam, Gaurav Datkhile and Mukesh D. Patil	201721047042	Dec-17	Electronics and Telecommunication Engineering
24	Laboratory setup for learning fractional order control system.	Dr. Vishwesh A. Vyawahare , Dr. M. D. Patil	201721044974	Dec-17	Electronics Engineering
25	Laboratory Setup for Learning Fractional-Order Control System	Vishwesh A. Vyawahare and Mukesh D. Patil	201721044974	Dec-17	Electronics and Telecommunication Engineering
26	A Technology (Process) For Identifying Attainable, Measurable And Verifiable Goals And Objectives FR	Dr. M. M. Bhatia	201721009024	Mar-17	Electronics Engineering

27	A technology (process) for identifying attainable, measurable and verifiable goals and objectives from vision and mission of an enterprise to enable measurement of attainment of Mission and Vision.	Dr. Vasappanavara Ramesh, Mrs. Chhabria Aditi Sunil, Dr. Bhatia Manmohan Singh	201721009024	Mar-17	Computer Engineering
28	Passive Contrast Enhancement detection using statistical learning approach for digital image (Idea Patent)	Mr. Gajanan Birajdar	201621022116	Jun-16	Electronics Engineering